

FIG. 1

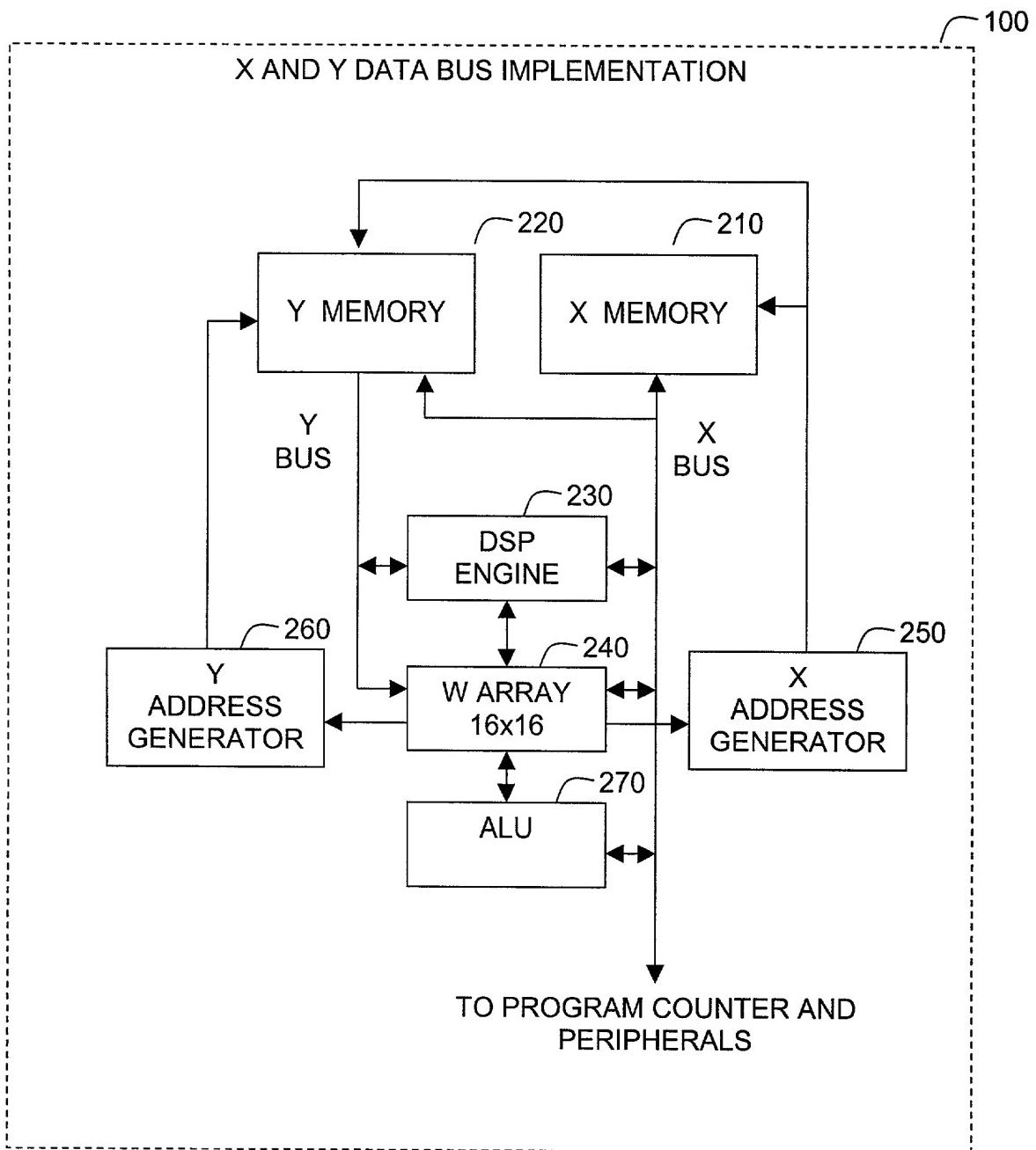


FIG. 2

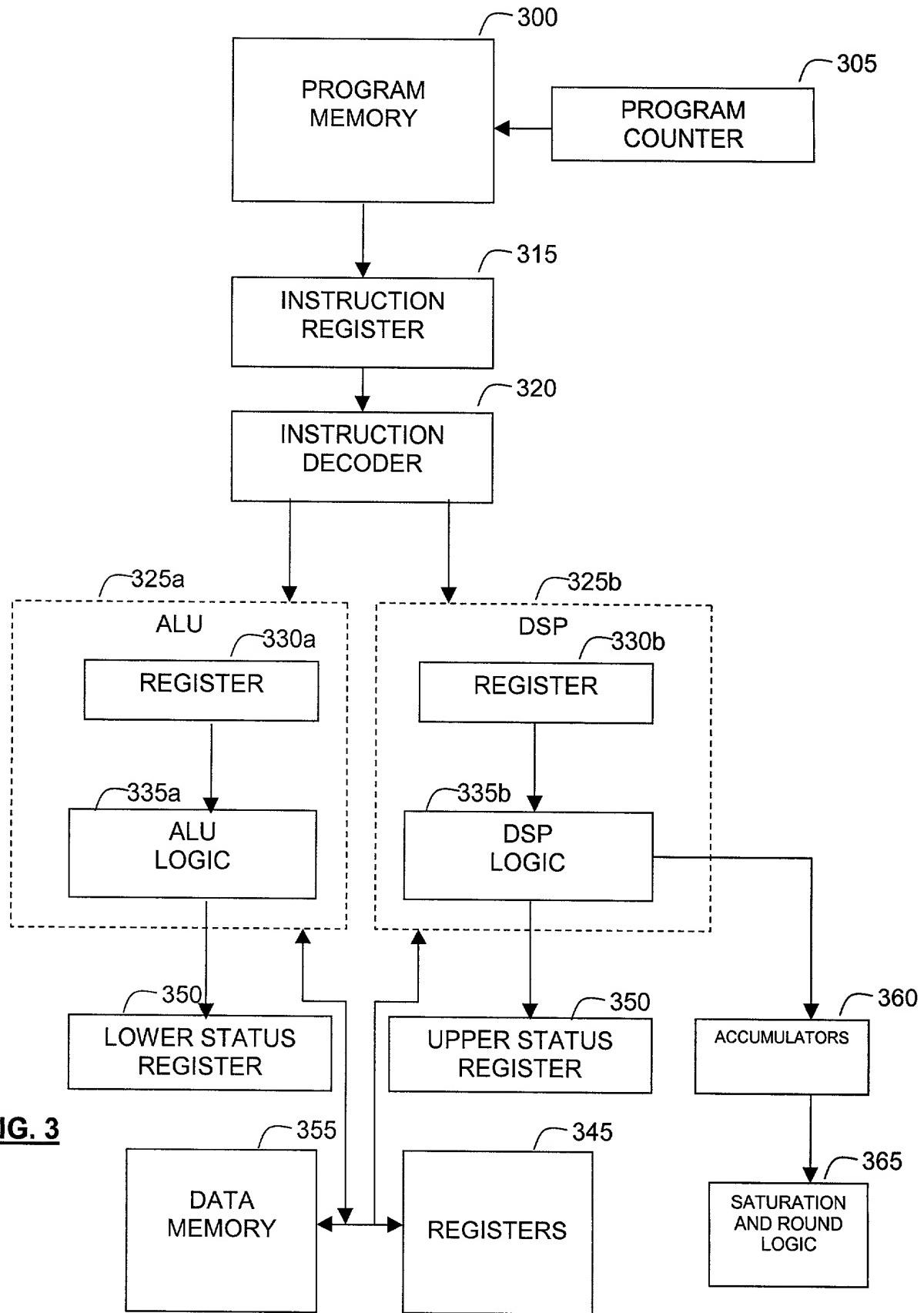


FIG. 3

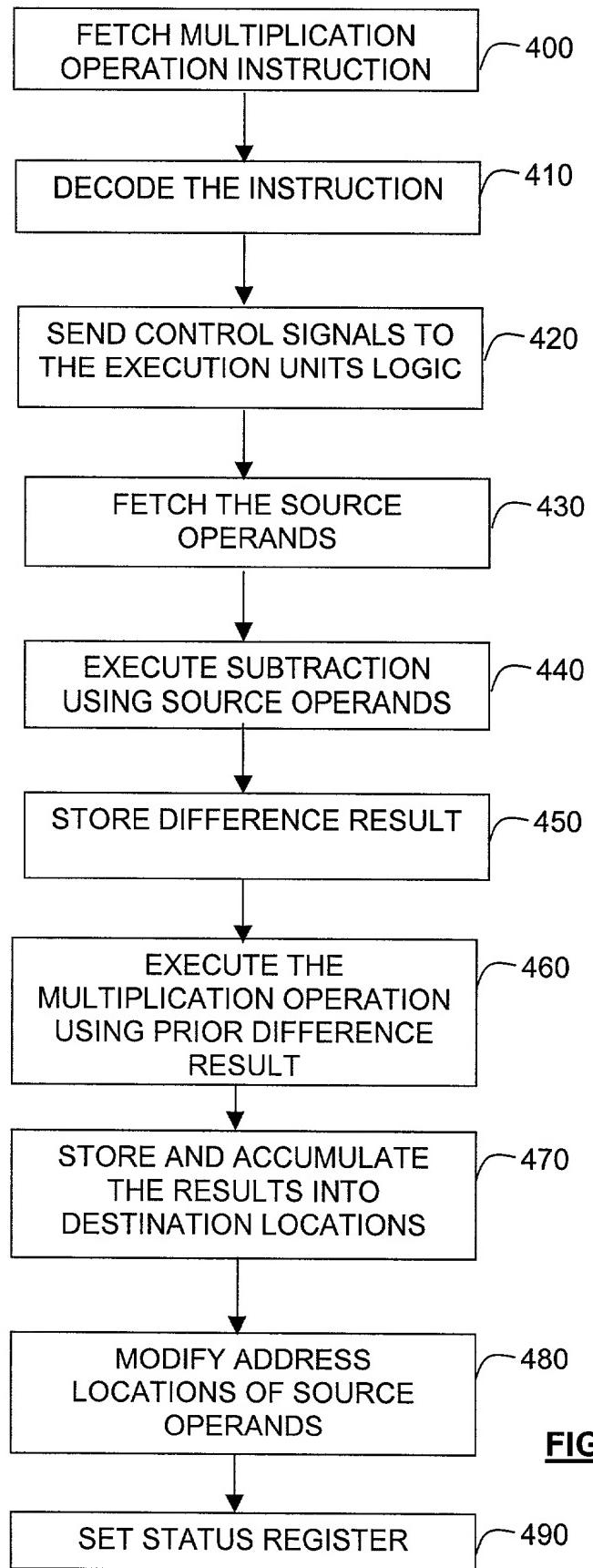


FIG. 4

Multiplication Operation Instructions

<u>SYNTAX</u>	<u>ENCODING</u>	<u>DESCRIPTION</u>
ED	1111 11mm A1xx 00ii ijjj jjii	computes $(A-B)^2$
EDAC	1111 11mm A1xx 00ii ijjj jjaa	computes $(A-B)^2 + C$

M bits- specifies register containing multiplication source operand

A bit- specifies destination location for square result

i bits- specifies register containing a difference operand, such as minuend

j bits- specifies register containing a difference operand, such as subtrahend

x bits- specifies destination location for difference result

FIG. 5